So luoc noi dung du kien

Guide 1:   -- Gioi thieu Kiem Thu va Thiet Ke Chip

Guide 2:   -- Cac nguon tham khao + Sach huong dan (Verilog/SystemVerilog) from Internet

             -- SoC (takes one day only)

Guide 3:   -- Design and Testbench/Verification (takes many weeks)

Guide 4:   -- Verification/UVM (takes many weeks)

Guide 5:   -- FPGA, Design and Verification (takes many weeks)

Guide 6:   -- ASIC, Design and Verification (takes some weeks or many)

Guide 7:   -- Machine Learning (takes weeks)

In summer, everyday's morning can be chosen for talking, except Sunday. After summer, just get all Saturday and half of Sunday only.

The beginning day could be right next Tuesday's morning (June 25, 2024) or any from that on.