

Verilog books

- A Verilog HDL Primer
 - <https://pdfcoffee.com/250671210-j-bhasker-a-verilog-hdl-primerpdf-pdf-free.html>
- Verilog HDL Synthesis A Practical Primer
 - <https://www.pdfdrive.com/verilog-hdl-synthesis-a-practical-primer-e25913840.html>
- Digital Logic Design Using Verilog - Coding and RTL Synthesis
 - <https://www.pdfdrive.com/digital-logic-design-using-verilog-coding-and-rtl-synthesis-e185825424.html>

SystemVerilog books

- Digital Integrated Circuit Design using Verilog and **SystemVerilog**
 - <https://www.pdfdrive.com/digital-integrated-circuit-design-using-verilog-and-systemverilog-e175251084.html>
- SystemVerilog for Verification - A Guide to Learning the Testbench Language Features
 - <https://www.pdfdrive.com/systemverilog-for-verification-a-guide-to-learning-the-testbench-language-features-third-edition-e161963894.html>
 - SystemVerilog Assertions and Functional Coverage: Guide to Language, Methodology and Applications
 - <https://www.pdfdrive.com/systemverilog-assertions-and-functional-coverage-guide-to-language-methodology-and-applications-e176062426.html>