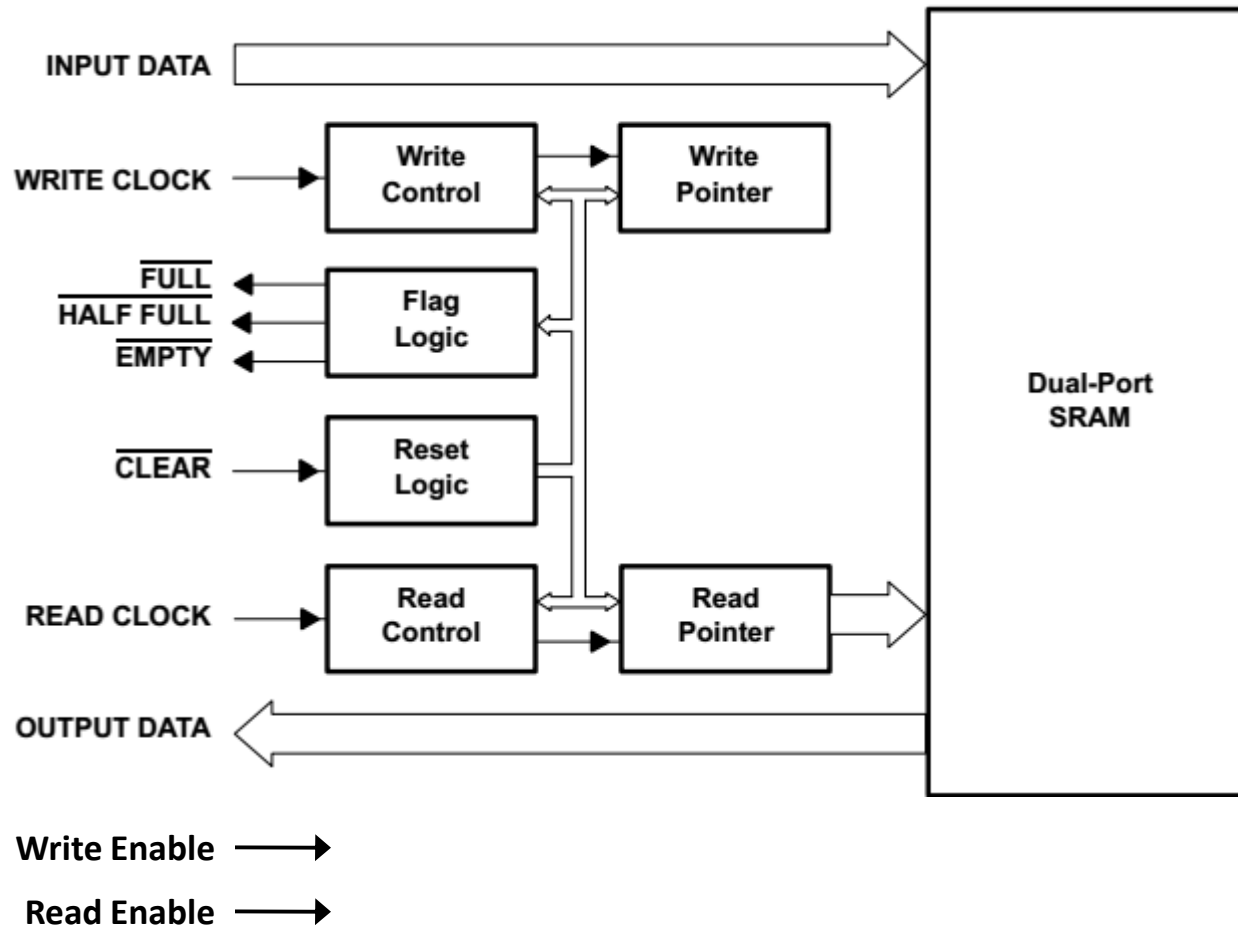


FIFO - Testbench

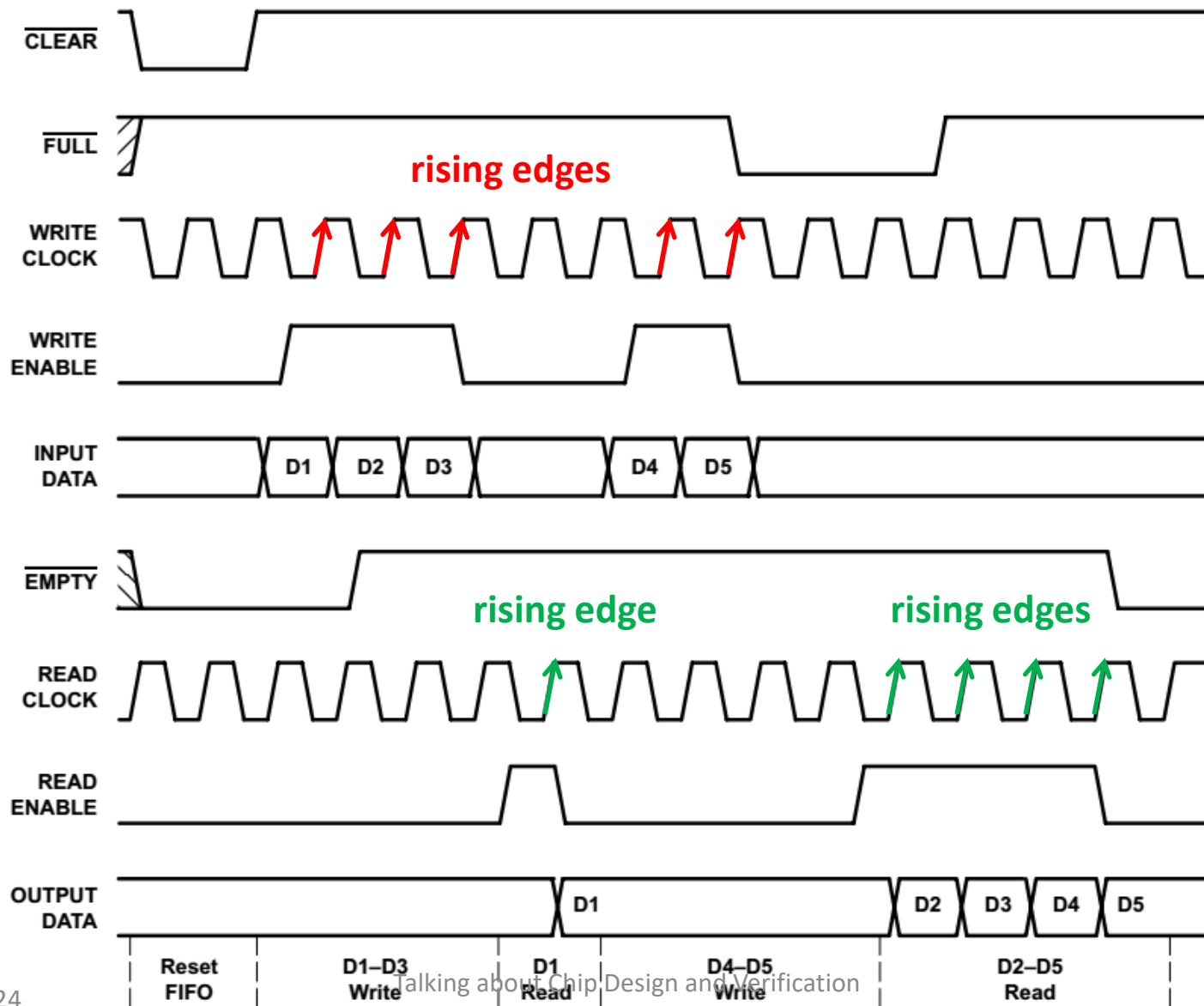
For SW Engineers

Tuan Nguyen-viet

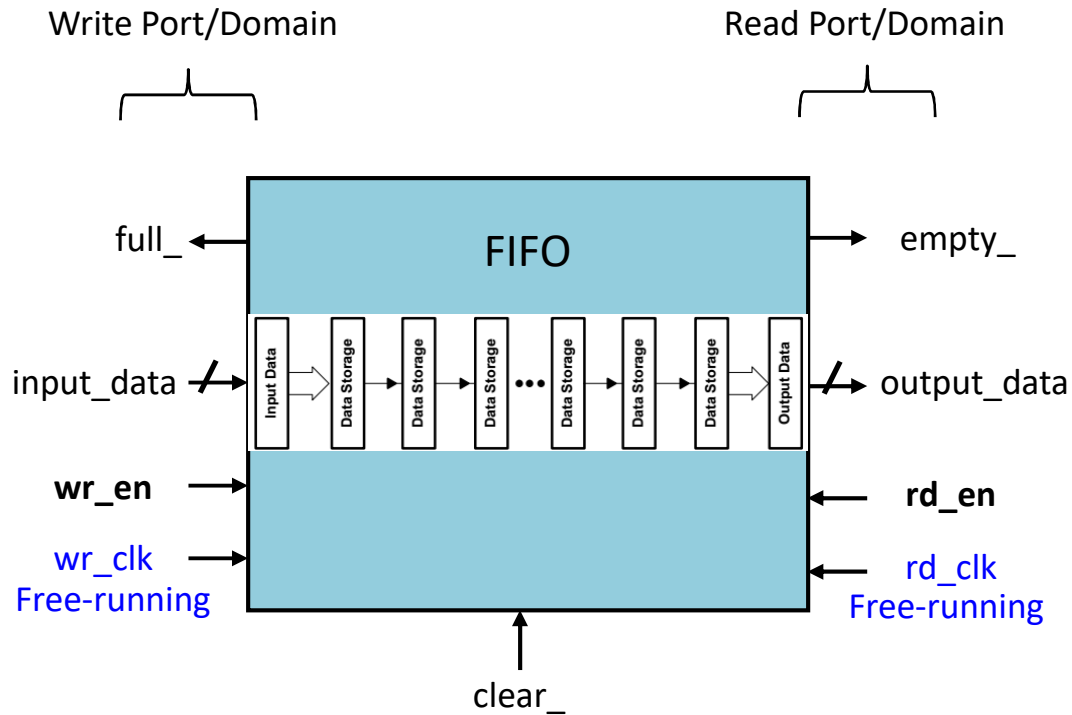
FIFO w/ Static Memory: Block Diagram



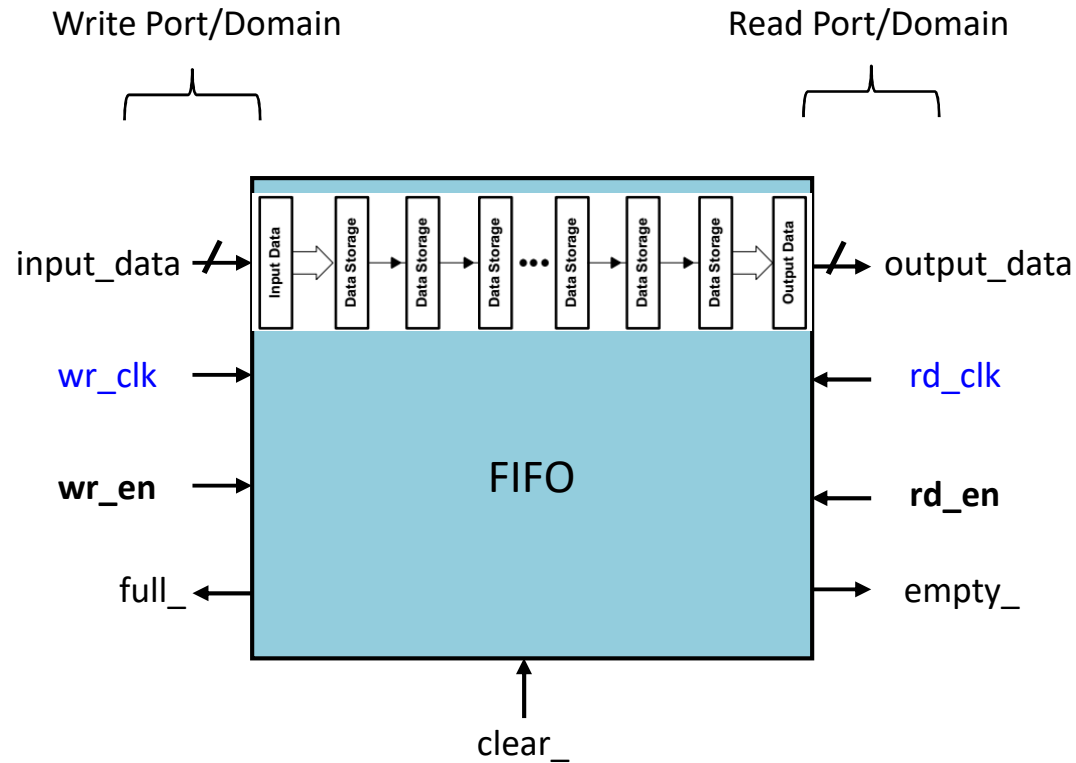
Timing Diagram for a Synchronous FIFO of Length 4



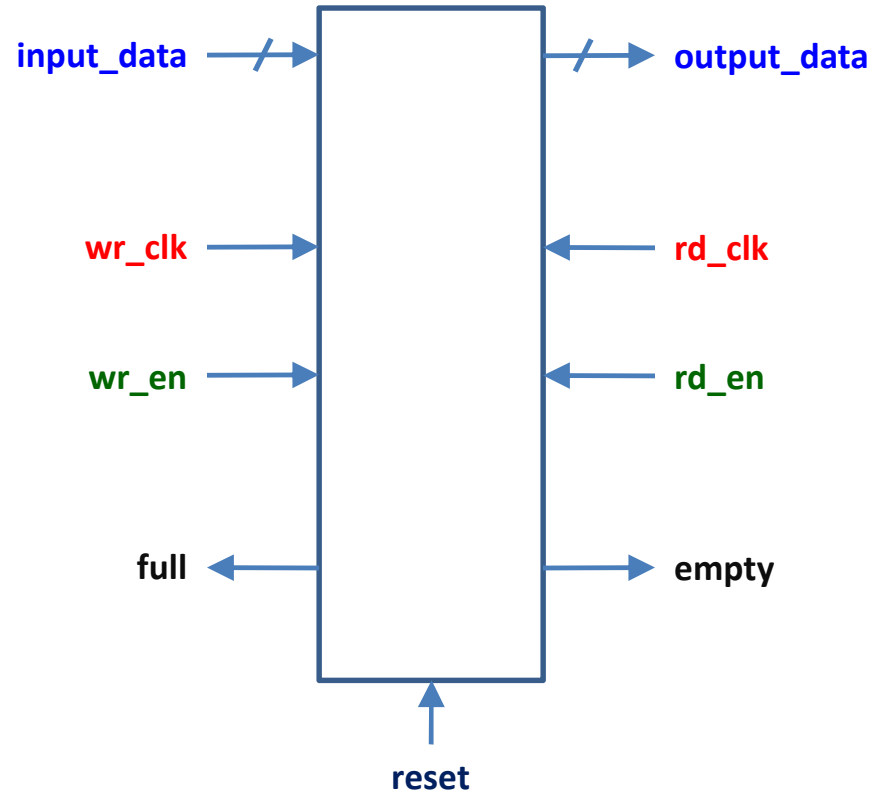
Synchronous FIFOs



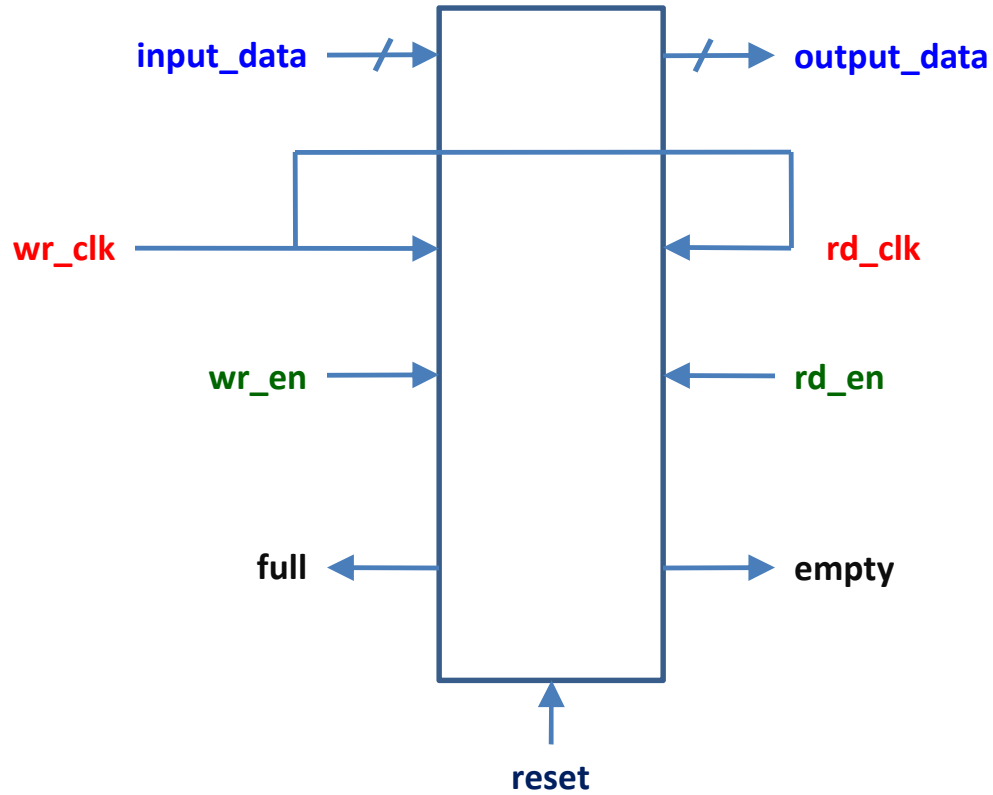
Synchronous FIFOs (2)



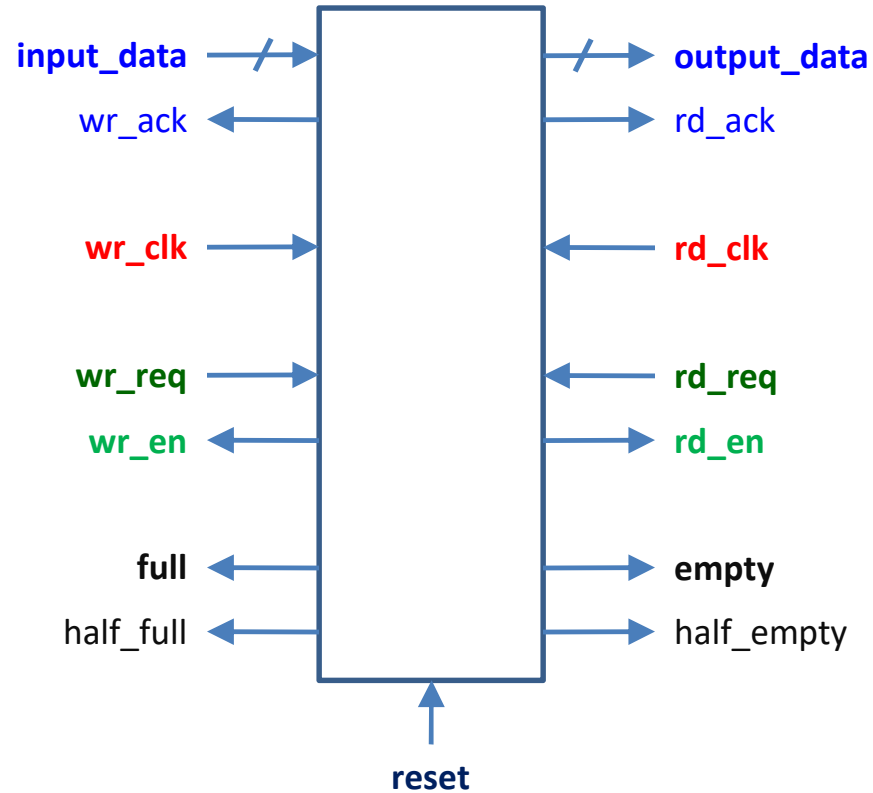
An FIFO



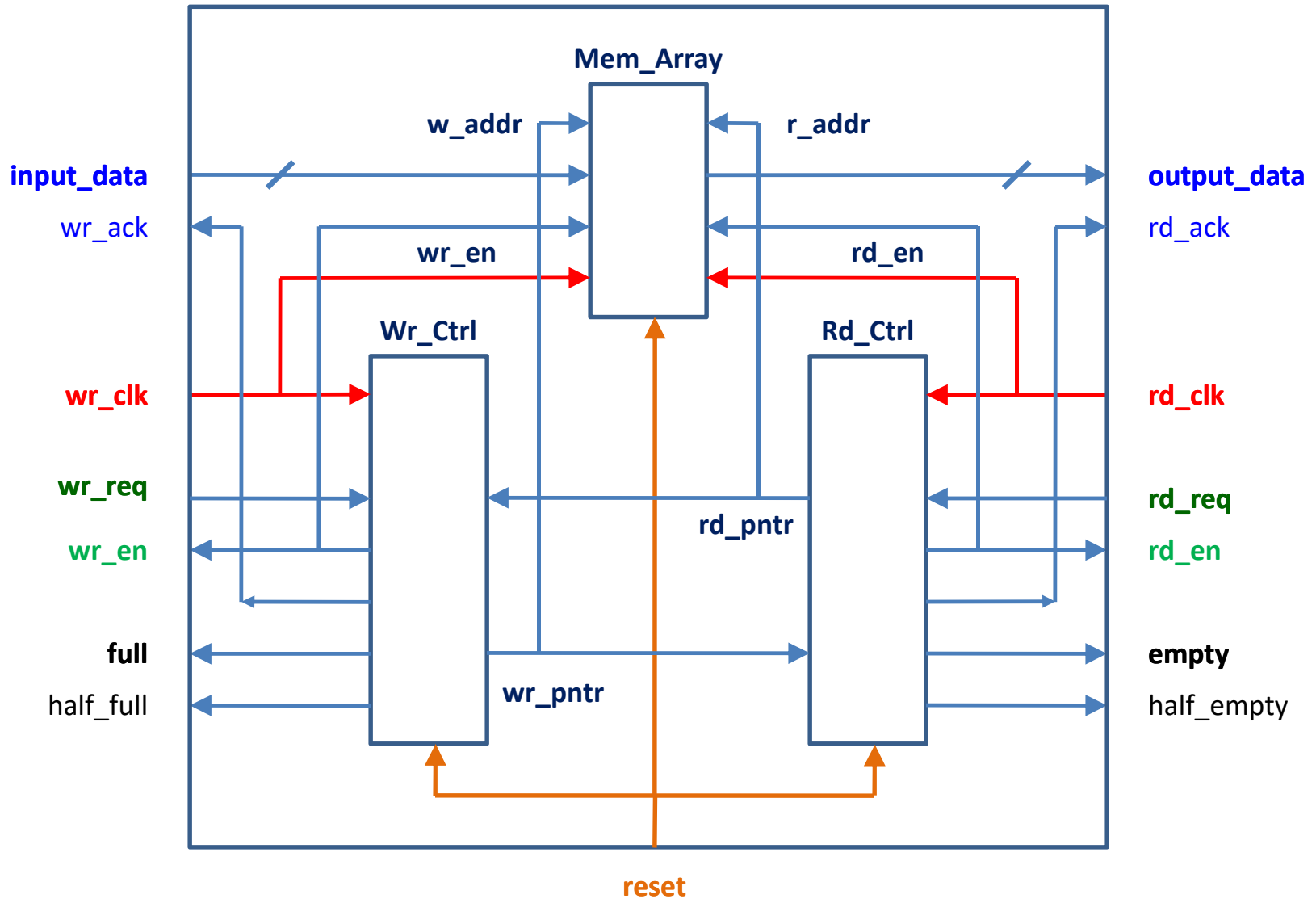
An FIFO (2)



An FIFO (3)



An FIFO (4)



Tasks

1. Do the testbench (**test_fifo.v**) for **DUT** written as **fifo_1.v**
2. Do the testbench (**test_fifo.v**) for **DUT** written as **fifo_2.v**

Thank You